Application No. 10/701,841
Paper Dated: February 4, 2004
Attorney Docket No. 1880-031249

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.

10/701,841

Applicant

:

Robert J. Hillard

Filed

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November 5, 2003

Title

METHOD OF ELECTRICAL CHARACTERIZATION

OF A SILICON-ON-INSULTAOR (SOI) WAFER

Group Art Unit

:

Not Yet Assigned

Examiner

Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the requirements of 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant submits this Information Disclosure Statement together with completed Form(s) PTO/SB/08A and a copy of each non-United States Patent or Patent Application Publication listed thereon.

Pursuant to the Notice regarding Information Disclosure Statements appearing in 1276 OG 55, dated August 5, 2003, no copy of each United States Patent or Patent Application Publication listed on the Form(s) PTO/SB/08A is included herewith.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 4, 2004.

Deborah L. Medves
(Name of Person Mailing Papers)

Signature

Date

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No fee is believed to be due for the filing of this Information Disclosure Statement as it is being submitted within three months of the filing of the application. Nevertheless, the Commissioner of Patents and Trademarks is hereby authorized to charge any additional fees which may be required to Deposit Account No. 23-0650. One (1) original and two (2) copies of this Information Disclosure Statement are enclosed.

Respectfully submitted,

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Complete if Known						
Application Number	10/701,841					
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First Named Inventor	Robert J. Hillard					
Group Art Unit	Not Yet Assigned					
Examiner Name	Not Yet Assigned					
Attorney Docket Number	1880-031249					

Sheet	t	1	of	2	Attorne	y Docket Number	1880-031249	
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